converters. Ivner, on the otherhand, discloses only that output converter circuitries are connected in series. The examiner states the same and thus appears to understand Ivner in the same manner. However, in the claims 1/6/7, it is required that input converter circuitries are connected in series. Ivner makes absolutely no even suggestion of this feature. See, for example, figures 1, 2, 5 and 7 of this invention versus figures 1 and 2 of Ivner. With respect to independent claim 3, note the recitation that the DC nodes of the third AC/DC converters in a plurality of converter cells are connected in series. In contrast, Ivner discloses only series connections for AC nodes of output converter circuitries. DC nodes of output converter circuitries are not connected in series in Ivner. Again, see figures 1 and 2 of the reference.

As can be seen, the combination of references does not suggest any of the claims of this application.

Should the examiner not allow the claims, it is respectfully requested that the finality of the prior rejection be withdrawn and that the next action be, if not the proper Notice of Allowance, then a final action.

The Commissioner is hereby authorized to charge any fees associated with this response or credit any overpayment to Deposit Account No. 13-3402.

Respectfully submitted,

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